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PATENT
JCS25 U.S. PTO
09/548942
04/13/00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Warren M. Farnworth

Serial No.: Not Yet Assigned

Filed: April 13, 2000

For: METHOD AND APPARATUS OF
INTERPOSING VOLTAGE REFERENCE
TRACES BETWEEN SIGNAL TRACES IN
SEMICONDUCTOR DEVICES

Examiner: Unknown

Group Art Unit: Unknown

Attorney Docket No.: 4161US (98-1265)

NOTICE OF EXPRESS MAILING

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Person making Deposit: Jared S. Turner

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as defined in

37 C.F.R. § 1.56(b). Further, no representation is made by Applicant herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

DOCUMENTS

U.S. Patent Documents

<u>U.S. Patent No.</u>	<u>Issue Date</u>	<u>Inventor</u>
3,679,826	07/1972	Crowell
3,990,102	11/1976	Okuhara et al.
4,349,848	09/1982	Ishii et al.
4,626,889	12/1986	Yamamoto et al.
4,651,163	03/1987	Sutera et al.
4,833,521	05/1989	Early
5,371,653	12/1994	Kametani et al.
5,451,917	09/1995	Yamamoto et al.
5,590,030	12/1996	Kametani et al.
5,675,298	10/1997	Bhagwan et al.
5,785,789	07/1998	Gagnon et al.
5,917,860	06/1999	Lee
5,945,886	08/1999	Millar
5,952,709	09/1999	Kitazawa et al.

Applicant offers to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: April 13, 2000

KCB/dlm

Enclosures: Form PTO-1449
Copy of documents cited

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Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>				Docket Number (Optional) 4161US (98-1265)		Application Number N t Y t Assigned	
				Applicant Warren M. Farnworth			
				Filing Date April 13, 2000		Group Art Unit Unknown	

1523 U.S. PTO
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 04/13/00

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,679,826	07/1972	Crowell			
	3,990,102	11/1976	Okuhara et al.			
	4,349,848	09/1982	Ishii et al.			
	4,626,889	12/1986	Yamamoto et al.			
	4,651,163	03/1987	Sutera et al.			
	4,833,521	05/1989	Early			
	5,371,653	12/1994	Kametani et al.			
	5,451,917	09/1995	Yamamoto et al.			
	5,590,030	12/1996	Kametani et al.			
	5,675,298	10/1997	Bhagwan et al.			
	5,785,789	07/1998	Gagnon et al.			

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

OTHER DOCUMENTS		(Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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